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Introduction This sample program is written entirely in IBM 370 Assembler. The program executes each of the problem-state, non-floating-point instructions in alphabetical sequence and will run as an MVS batch job on an IBM mainframe or as a project with Micro

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Focus Mainframe Express (MFE) running on a Windows System.

IBM Basic Assembly Language and successors - Wikipedia

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The latest derived language is known as the IBM High-Level Assembler (HLASM). Programmers utilizing this family of assemblers also refer to them as ALC, (for Assembly Language Coding), or simply "assembler". BAL is also the mnemonic of the "Branch And Link"

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instruction. 1 General characteristics

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Introduction to the New Mainframe: z/OS Basics March
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SG24-6366-02

BAL: IBM 360 Assembly Language - Baylor ECS
This chapter describes, in detail, the syntax and
usage rules of each assembler instruction. There is
also information about assembly instructions on
Conditional assembly instructions. The following table
lists the assembler instructions by type, and provides

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the number of the page where the instruction is described.

IBM MAINFRAME: XCTL, LINK, LOAD, BALR (BASR) and CALL.

Technicians and operators carried cards with commonly needed information culled from the many reference manuals for each computer. A “green card” in your shirt pocket announced that you were an assembly-language programmer.

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BAL (always spelled out: BEE-AY-EL) stands for Basic Assembly Language. This is the assembly language

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for the IBM 360/370/390 series of computers. One cannot program in assembly language without using some operating-system-specific coding. If nothing else, one must obey the proper linkage conventions.

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The 370 Instruction Set, Assembler Coding Examples
BALR/BASR are used in Assembler and are also used
in CICS (after expansion). A static call (CALL Macro) is

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when a VCON address (module is merged with the caller) is loaded into R15, followed by a BALR/BASR 14,15. A dynamic call is when a LOAD is first performed, then the LP address is loaded into R15, followed by a BALR/BASR 14,15.

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IBM MAINFRAME: IBM Assembler- Tutorial, References

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Front cover Introduction to the New Mainframe - IBM Redbooks

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Ibm Principles Of Operation Manual GA22-6821, and IBM. System/ 3 70 Principles of Operation, Order No. This manual was prepared by the IBM General Products Division, Technical Publishing. The IBM z13 vector extension facility brings SIMD instructions back to mainframe in chapters 21 through 24 of the z13 Principles of Operation manual.

IBM Offering Information

BALR is a Branch and Link Register Instruction for use in transferring to different locations for code running in 24- 31, or 64-bit mode. It stores the address of the next sequential instruction in the target register, then jumps to the address specified in the source_register,

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unless register 0 is specified, in which case no branch is taken.

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